

controller 11 and is therefore not supplied with picture information, i.e. the pixel lines 14 belonging to the partial area 16 of the display 13 remain dark. This procedure is repeated with each refresh cycle.

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Alternatively, it is also possible in a corresponding manner to connect the display controller 11 externally to a counter 12, as shown by the broken line in Fig. 1. This means that, in addition to the conventional chip of the display controller 11, a control counter 12 is provided, which also counts the pixel lines processed by the display controller 11. Once the display controller 11 in standby mode has processed the pixel lines belonging to the partial area 15, for example the first 20 pixel lines of the display 13, it is disabled by the control counter 12. Similar to the first variant described above, the remainder of the display 13, i.e. the pixel lines 14 belonging to the partial area 16, are not processed in this case by the display controller 11, so that these pixel lines are not supplied with picture information. In this case also, this procedure is repeated with each refresh cycle, whereby the counter level of the counter 12 is reset at the start of each refresh cycle.

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The control of the display 13 is of course not restricted to the embodiment explained with reference to Fig. 2, in which entire pixel lines are allocated to the partial areas 15 and 16. It is also possible for a group of individual pixels of the display 13 to be allocated to the partial areas 15 and 16 without this group forming entire pixel lines. In this case, the display controller 11 and the counter 12 would have to be adapted compared with the above description in such a way that individual pixels of the matrix-type display 13, rather than entire pixel lines, are counted and monitored, whereby, similar to the above procedure, on

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reaching a pixel allocated to the partial area 16, the processing of this pixel is suppressed by the display

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controller 11 so that no picture information is supplied to this pixel.

Thus, for example, the partial area 15 provided for the presentation of user information and status information can be disposed on a lateral edge of the matrix-type display 13 which is divided into pixel lines 14 and pixel columns 17. This embodiment is shown in Fig. 3.

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- 10 It is similarly possible, for example, to allocate a group of pixels provided in a corner area of the display 13 to the partial area 15 for the presentation of user or status information. This embodiment is shown in Fig. 4.

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